

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>527</b>	<b>257/783.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>85</b>	<b>1 and (alumina ("al.sub.2 o.sub.3"))</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>22</b>	<b>2 and (thin adj film\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>63</b>	<b>2 not 3</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>216</b>	<b>1 and ceramic\$1</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>37</b>	<b>5 and (thin adj film)</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>131</b>	<b>5 not (2 6)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L8</b>	<b>278</b>	<b>1 and ((wire adj bond\$3) (wirebond\$3))</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>90</b>	<b>8 and ((die adj attach\$4) (die adj receiving))</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>39</b>	<b>9 not (2 6 7)</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>319</b>	<b>257/782.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>38</b>	<b>11 and (alumina ("al.sub.2 o.sub.3"))</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>127</b>	<b>11 and ceramic\$1</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L14</b>	<b>69</b>	<b>11 and ((die adj attach\$4) (die adj receiving))</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>15</b>	<b>L15</b>	<b>30</b>	<b>14 not (12 13)</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L16</b>	<b>156</b>	<b>11 not (2 6 7 10 12 13 14)</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L17</b>	<b>851</b>	<b>257/784.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L18</b>	<b>774</b>	<b>17 not (2 6 7 10 12 13 14 16)</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L19</b>	<b>54</b>	<b>18 and (alumina ("al.sub.2 o.sub.3"))</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L20</b>	<b>197</b>	<b>18 and ceramic\$1</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L21</b>	<b>213</b>	<b>19 20</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>22</b>	<b>L22</b>	<b>661</b>	<b>17 and (wire\$1 with bond\$3)</b>	<b>USP AT; US-P GPU B</b>
<b>23</b>	<b>L23</b>	<b>480</b>	<b>22 not 21</b>	<b>USP AT; US-P GPU B</b>
<b>24</b>	<b>L24</b>	<b>63</b>	<b>23 and ((die adj attach\$4) (die adj receiving))</b>	<b>USP AT; US-P GPU B</b>